



SIMATIC DP, Electronics module for ET 200S, 2 AO I 15 mm width, +/-20mA; 13 bit+sign, 4..20mA; 13 bit Cycle time less than 1 ms with SF LED (group fault)

General information	
Product function	
<ul style="list-style-type: none"> <li>• Isochronous mode</li> </ul>	No
Supply voltage	
Load voltage L+	
<ul style="list-style-type: none"> <li>• Rated value (DC)</li> </ul>	24 V; From power module
<ul style="list-style-type: none"> <li>• Reverse polarity protection</li> </ul>	Yes
Input current	
from load voltage L+ (without load), max.	150 mA
from backplane bus 3.3 V DC, max.	10 mA
Power loss	
Power loss, max.	2 W
Address area	
Address space per module	
<ul style="list-style-type: none"> <li>• Address space per module, max.</li> </ul>	4 byte
Analog outputs	
Number of analog outputs	2
Current output, no-load voltage, max.	18 V
Cycle time (all channels) max.	1.5 ms
Output ranges, current	
<ul style="list-style-type: none"> <li>• -20 mA to +20 mA</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• 4 mA to 20 mA</li> </ul>	Yes
Connection of actuators	
<ul style="list-style-type: none"> <li>• for current output two-wire connection</li> </ul>	Yes
<ul style="list-style-type: none"> <li>• for current output four-wire connection</li> </ul>	No
Load impedance (in rated range of output)	
<ul style="list-style-type: none"> <li>• with current outputs, max.</li> </ul>	500 Ω
<ul style="list-style-type: none"> <li>• with current outputs, inductive load, max.</li> </ul>	1 mH
Destruction limits against externally applied voltages and currents	
<ul style="list-style-type: none"> <li>• Voltages at the outputs towards MANA</li> </ul>	15 V; max. 15 V continuous; 75 V for max. 1 s (mark to space ratio 1:20)
<ul style="list-style-type: none"> <li>• current / at the analog outputs / as destruction limit for externally applied voltage / maximum permissible</li> </ul>	50 mA; DC
Cable length	
<ul style="list-style-type: none"> <li>• shielded, max.</li> </ul>	200 m
Analog value generation for the outputs	
Integration and conversion time/resolution per channel	
<ul style="list-style-type: none"> <li>• Resolution with overrange (bit including sign), max.</li> </ul>	14 bit; 4 to 20 mA: 13 bit, ±20 mA: 14 bit
Settling time	
<ul style="list-style-type: none"> <li>• for resistive load</li> </ul>	0.1 ms
<ul style="list-style-type: none"> <li>• for capacitive load</li> </ul>	0.5 ms

• for inductive load	0.5 ms
<b>Errors/accuracies</b>	
Operational error limit in overall temperature range	
• Current, relative to output range, (+/-)	0.5 %
Basic error limit (operational limit at 25 °C)	
• Current, relative to output range, (+/-)	0.3 %
<b>Interrupts/diagnostics/status information</b>	
Diagnoses	
• Diagnostic information readable	Yes
• Wire-break	Yes
• Group error	Yes
Diagnostics indication LED	
• Group error SF (red)	Yes
<b>Parameter</b>	
Remark	7 byte
Diagnostics wire break	Disable / enable
Group diagnostics	Disable / enable
Response to CPU/master STOP	Output current and de-energized/substitute a value/keep last value
<b>Potential separation</b>	
Potential separation analog outputs	
• between the channels	No
• between the channels and backplane bus	Yes
• Between the channels and load voltage L+	Yes
<b>Isolation</b>	
Isolation tested with	500 V DC
<b>Dimensions</b>	
Width	15 mm
Height	81 mm
Depth	52 mm
<b>Weights</b>	
Weight, approx.	40 g

**last modified:** 9/11/2023 